Amendments To The Drawings:

The attached drawing sheets include changes to FIGS. 1, 2, 4A and 4B.

The annotated sheets show revisions, highlighted in yellow for the examiner's approval. The clean sheets are requested to replace the original sheets of FIGS. 1, 2, 4A and 4B.

Attachment: Four (4) Replacement Sheet(s) of: FIG. 1 (one sheet), FIG. 2 (one sheet), FIGS. 3 and 4A (one sheet) and FIG. 4B (one sheet).

Annotated Sheet(s) Showing Changes to FIGS. 1, 2, 4A and 4B.

REMARKS/ARGUMENTS

Claims 1-3 are pending in this application.

In the Office Action, the Examiner objected to the disclosure because "400" on line 1 of paragraph [0020] should have been "300." In addition, the term "LCD module" in line 3 of paragraph [0020] should have been described as the "conversion board device."

As can be seen above, paragraph [0020] has been amended to overcome the Examiner's objections. Paragraph [0028] has also been amended however to correct a typographical error in line 12 of page 7. Paragraph [0028] has been amended to change "VCOMP_CT" to "VCOM_CT."

The drawings were also objected to.

The Examiner noted that a connection is not shown between the "INPUT UNIT" and the "TIMING CONTROLLER" and "VOLTAGE GENERATING UNIT" on both FIG. 1 and FIG. 2. Correction has been made to FIG. 1 and FIG. 2 as shown in the attached drawing sheets.

In addition, reference numeral "108" has also been added to both FIG. 1 and FIG. 2. No new matter has been added to either FIG. 1 or FIG. 2.

The legend "PRIOR ART" has been added to FIG. 4A and FIG. 4B.

As for the claims, the Examine rejected claims 1-3 under 35 U.S.C. §103(a) as being unpatentable over the admitted prior art (APA) and U.S. publication number 2001/0043203 to Iinuma.

The Examiner contends that all of the claim limitations have been admitted as prior art except for a scaler having a GPIO port. The Examiner

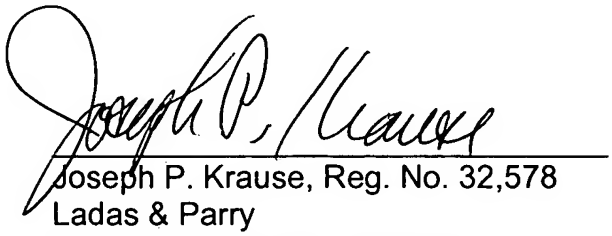
cited Iinuma as showing a scaler having a GPIO port. The Applicant disagrees with the Examiner's reading of Iinuma but has nevertheless amended claim 1 to avoid the Iinuma reference.

As shown above, claim 1 now recites a particular type or kind of scaler, namely, a scaler that digitizes inputs to the LCD module and which scales said input signals to the LCD module to match the LCD module. The scaler has a GPIO port. Thus, as claim 1 is amended, it requires a particular scaler.

Iinuma on the other hand is directed to power saving and does not mention, show or suggest a "scaler" that "digitizes inputs to the LCD module" and which "scales input signals...to match [an] LCD module." Thus, Iinuma and the APA does not satisfy all of the pending claim limitations.

Since neither the APA nor Iinuma show or suggest a scaler that is now recited in claim 1, claim 1 and claims 2-3 are allowable over the prior art of record. Reconsideration of claims 1-3 is respectfully requested.

Sincerely,



Dated: November 2, 2006

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Ladas & Parry
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FIG.1

(RELATED ART)

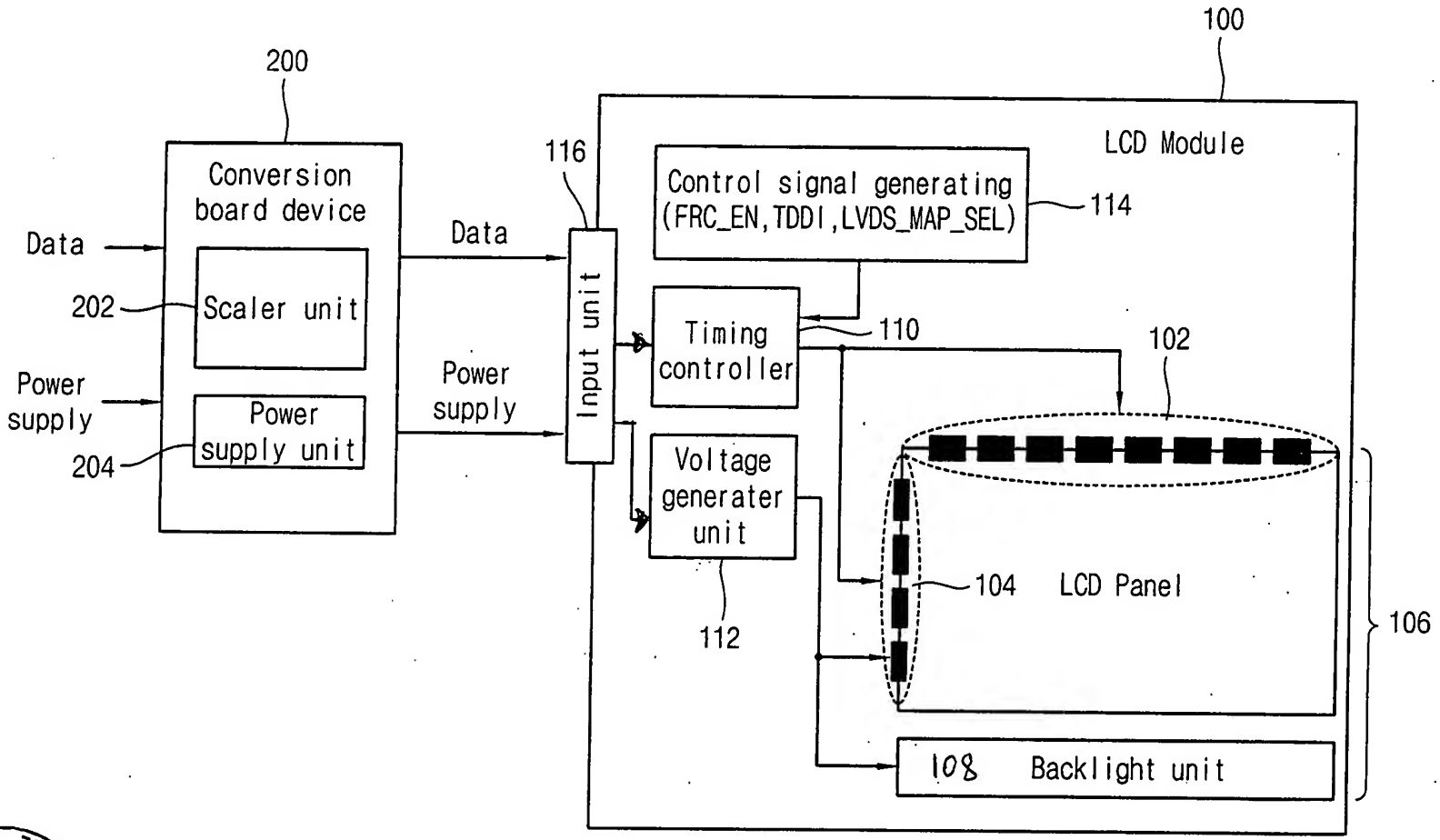

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FIG.2

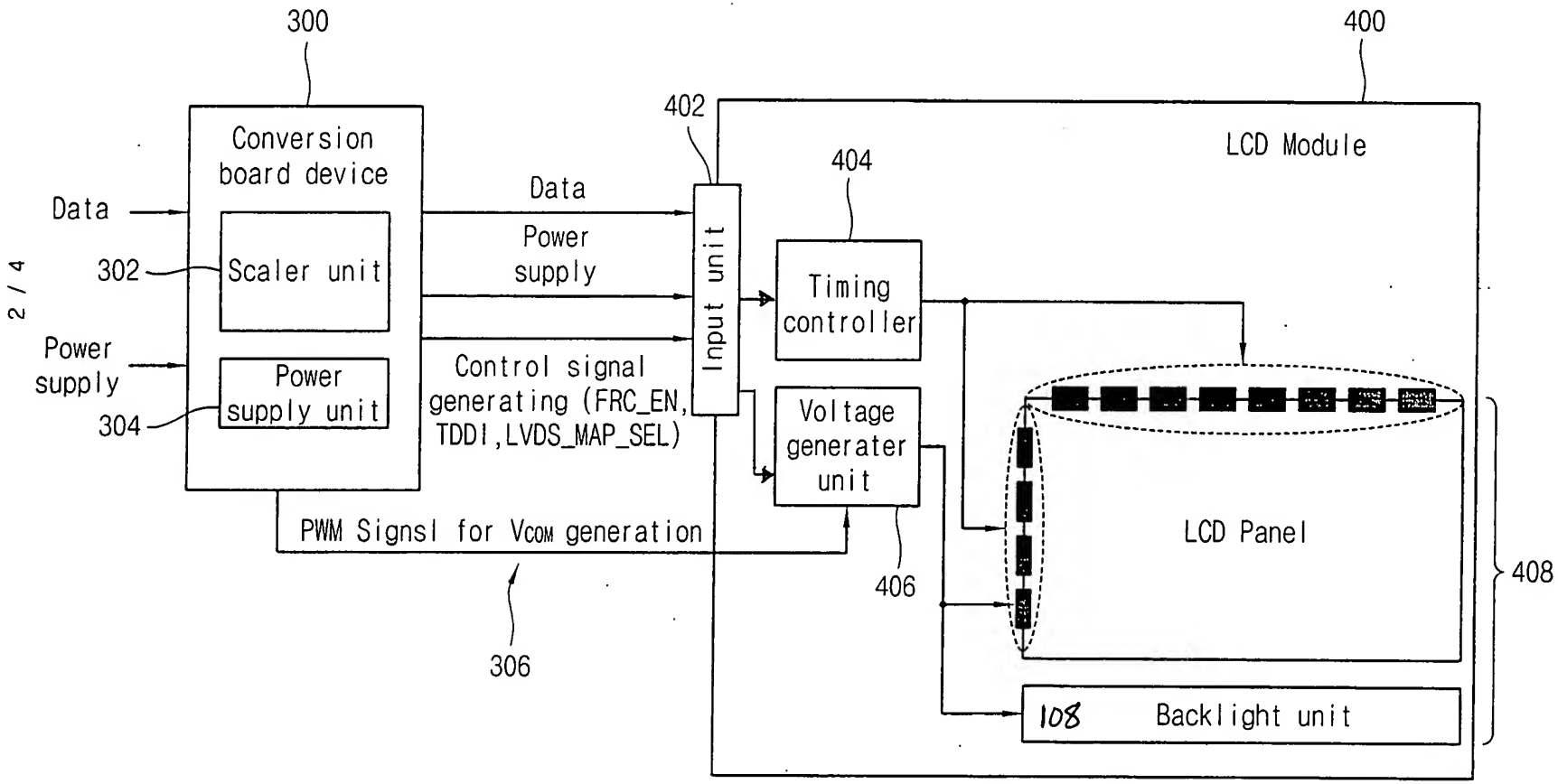
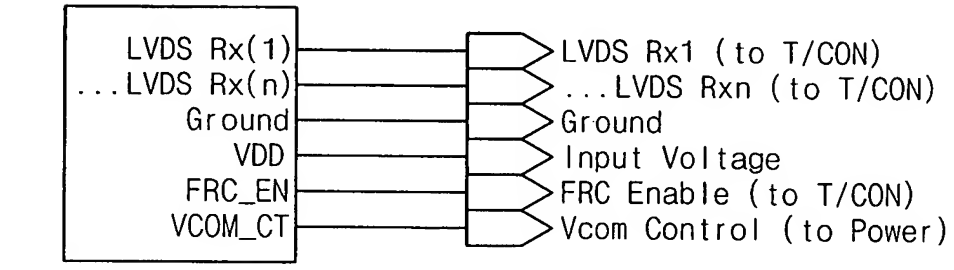

300
400
402
Conversion board device
LCD Module
404
Data
Data
302
Scaler unit
Power supply
Input unit
Timing controller
Power supply
Power supply unit
304
Control signal generating (FRC_EN, TDDI, LVDS_MAP_SEL)
Voltage generater unit
LCD Panel
408
PWM Signsl for VCOM generation
406
306
108
Backlight unit
2 / 4

FIG.3

Name	Function	Function
FRC_EN	Panel Dithering On/Off	High=Enable Low=Disable
VCOM_CT	Vcom control On/Off	
TDDI	TDDI On/Off	
POL_SEL	1Line POL or 2Line POL Sel.	
LVDS_MAP_SEL	LVDS_MAP_SEL	

FIG.4A

(PRIOR ART)



LCD Module
device input part (402)

FIG.4B

(PRIOR ART)

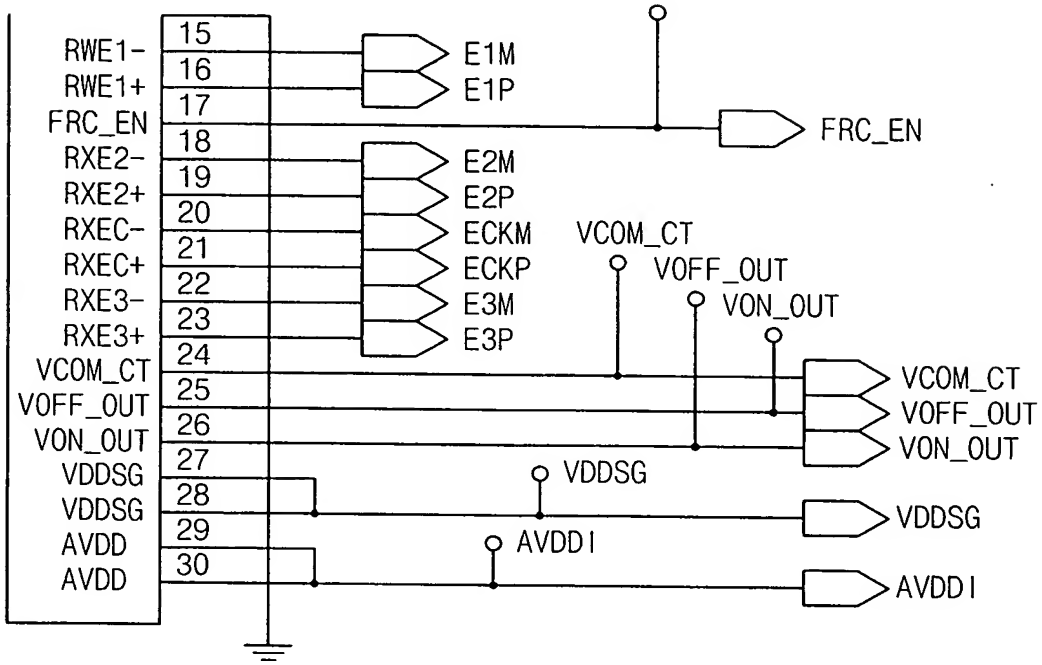
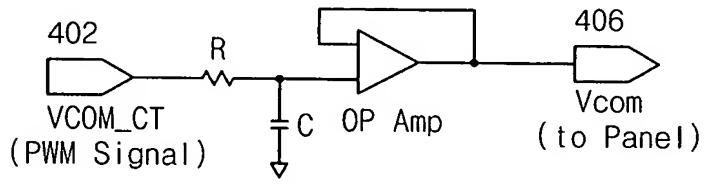



FIG.5